

Fig. 1

Prior Art

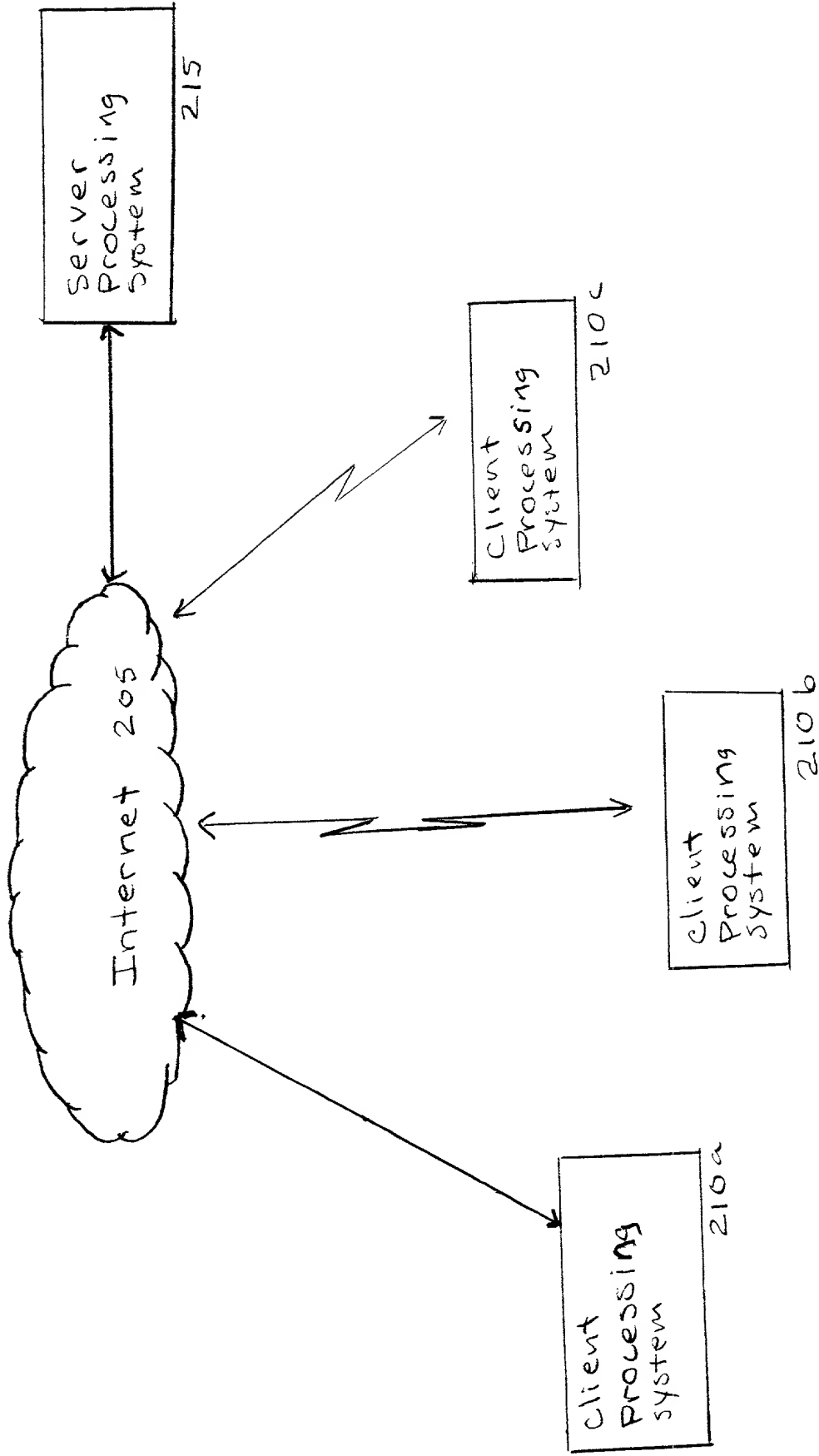
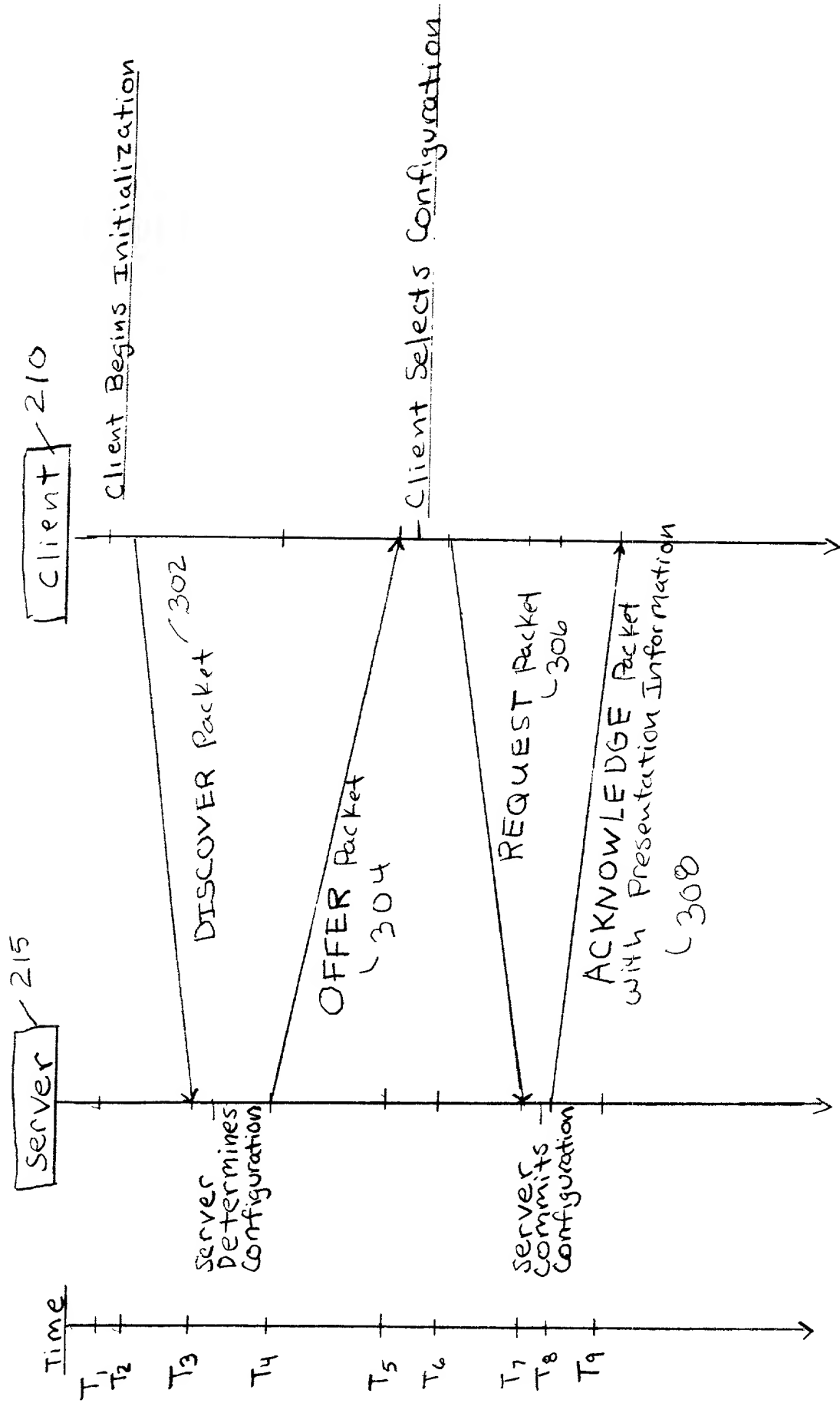


Fig. 2



Message Flow Time Line

FIG 3

FIG. 4A

400

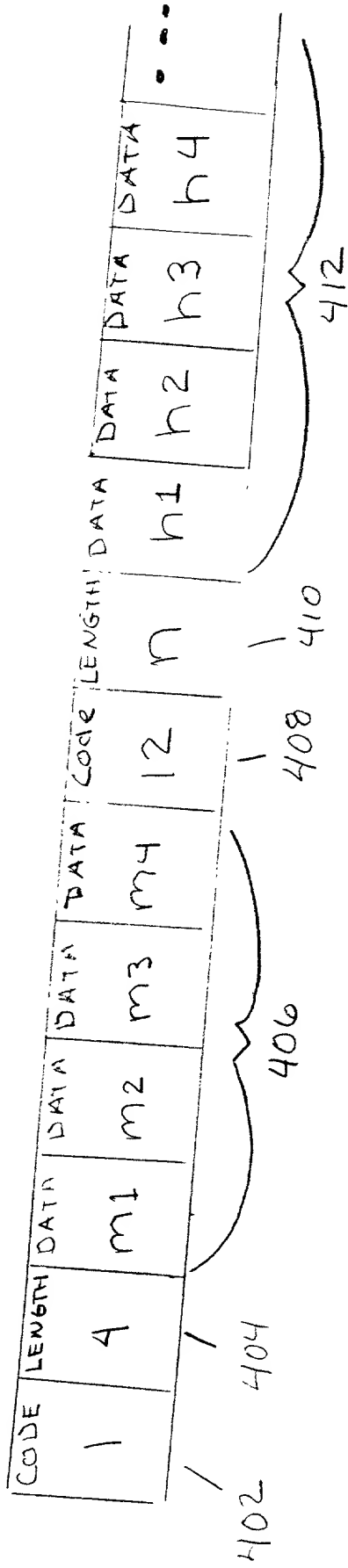
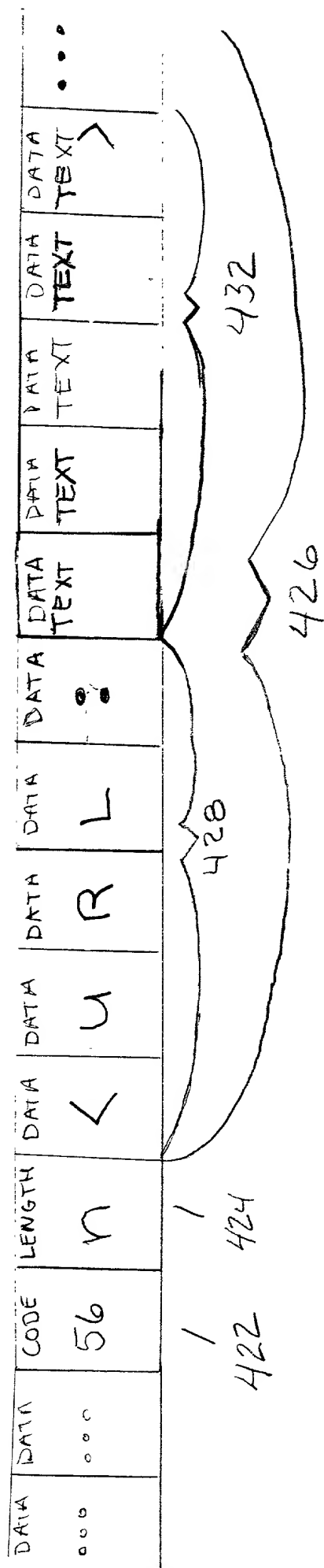


Fig. 4A

DHCP Packet Format



4B
5
11

DHCP Option Code 56

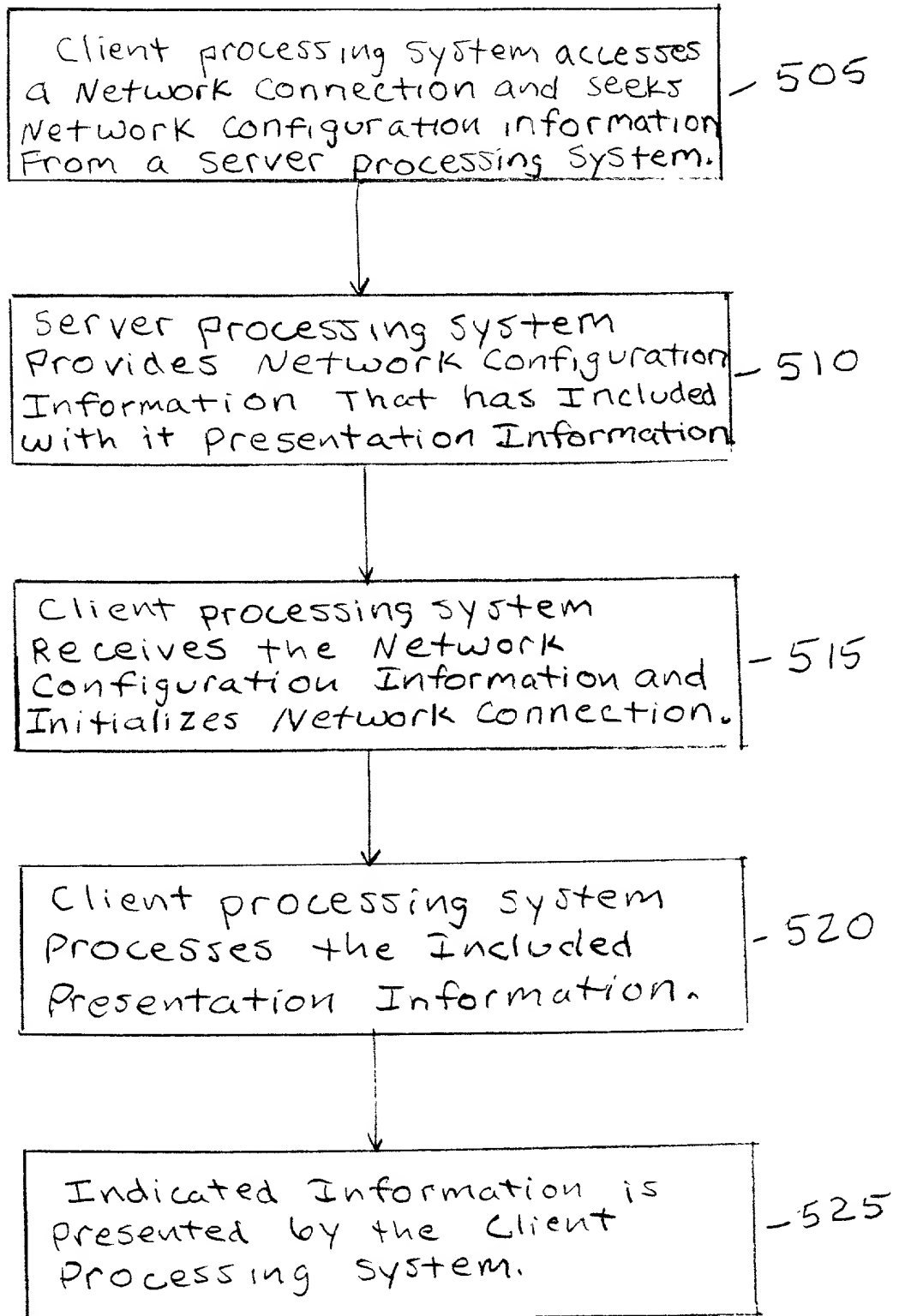


Fig. 5

Digital Processing System (DPS) Block Diagram

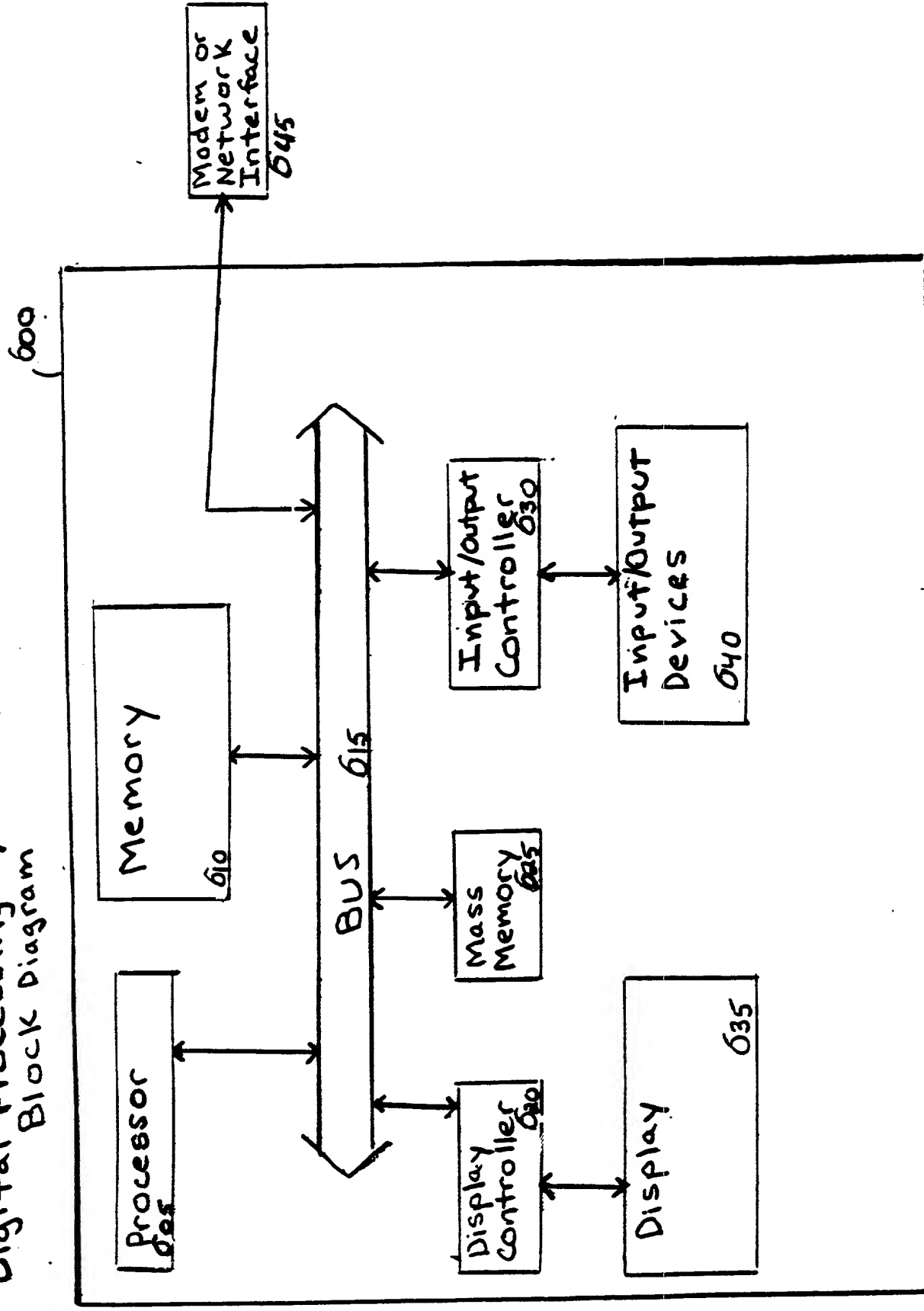


Fig. 6